

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

1. (cancelled)

2. (cancelled)

3. (cancelled)

4. (cancelled)

5. (cancelled)

6. (cancelled)

7. (currently amended) A logic drawing entry apparatus for processing a plurality of drawing sheets for computer aided design of logic circuits, each of said plurality of drawing sheets indicating a logic circuit having at least one symbol, comprising:

means for creating an inter-drawing diagram file which describes respective positions of said plurality of drawing sheets on one screen and attributes of said plurality of drawing sheets;

inter-drawing indication means for indicating, on said screen, said plurality of drawing sheets according to the description in said inter-drawing diagram file by miniaturizing the size of each of said plurality of drawing sheets;

inter-drawing connection counting means for counting the number of connections between any combinations of said two of said plurality of drawing sheets, wherein a connection describes the relationship between two symbols on the two of said plurality of drawing sheets;

net connection relation drawing means for drawing nets among said plurality of drawing sheets miniaturized and indicated on said screen, an indication of said nets being modified

based on the number of connections counted by said inter-drawing connection counting means;
and

~~The logic drawing entry apparatus of claim 5, further comprising:~~

drawing name ~~modifying~~ changing means for selecting a plurality of said drawings sheets and ~~modifying~~ changing the name of said plurality of drawing sheets, in ascending or descending order.

8. (currently amended) The logic drawing entry apparatus of claim 7, wherein said drawing name ~~modifying~~ changing means, further, designates intervals between the names of said plurality of drawing sheets.

9. (cancelled)

10. (cancelled)

11. (currently amended) A logic drawing entry apparatus for processing a drawing sheet indicating a logic circuit which has a plurality of symbols and nets connecting among said symbols, logic drawing entry apparatus comprising:

symbol selecting means for selecting via a user a symbol to be moved and a position to which the selected symbol moves;

judging means for judging whether or not a symbol exists at said position selected;

symbol moving means moving said selected symbols to said position if there is no symbol at said selected position;

symbol swapping means for swapping said selected symbol for a symbol at said selected position if there is a symbol at said selected position, so that positions of said selected symbol and said symbol at said selected position are swapped with each other;

net redrawing means for redrawing nets for said selected symbols after the movement or swap while keeping the connection relations between said selected symbols before the movement; and

~~The logic drawing entry apparatus of claim 9, further comprising~~

arranging means for arranging a plurality of selected symbols on said drawing sheet to form a column or a row.

12. (previously presented) The logic drawing entry apparatus of claim 11, wherein said arranging means, further, designates intervals between symbols.

13. (cancelled)

14. (currently amended) A logic drawing entry apparatus for drawing a plurality of drawing sheets for hierarchical design of a logic circuit, comprising:

~~judging a determination means determining whether to determine if a particular level of hierarchical design of a logic circuit comprises a plurality of sub-symbols, each of which belongs to the same level as said particular level;~~

a drawing means for drawing, upon said determination, a plurality of different drawing sheets by dividing said plurality of sub-symbols into individual sub-symbols so that each of said different drawing sheets only includes any one of said sub-symbols, if said judging means determines that said particular level of hierarchical design of a logic circuit comprises a plurality of symbols, each sub-symbol of which belongs to the same level as said particular level; and

a net drawing means for drawing nets for each of said symbols.

15. (cancelled)

16. (cancelled)

17. (cancelled)

18. (cancelled)